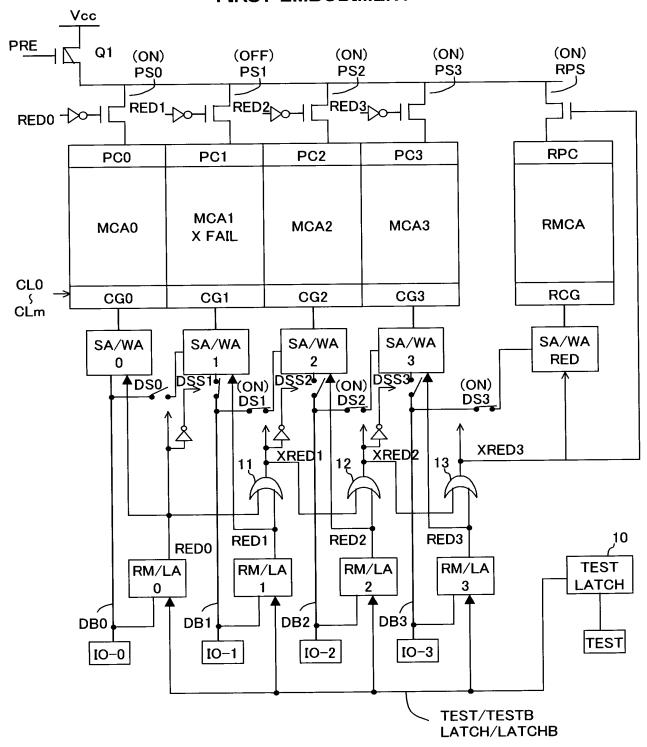
Title: MEMORY CIRCUIT WITH REDUNDANT MEMORY CELL ARRAY ALLOWING SIMPLIFIED SHIPMENT TESTS AND REDUCED

POWER CONSUMPTIONS Inventor: TANISHIMA et al. Application No. New Docket No. 108066-00096

FIG. 1

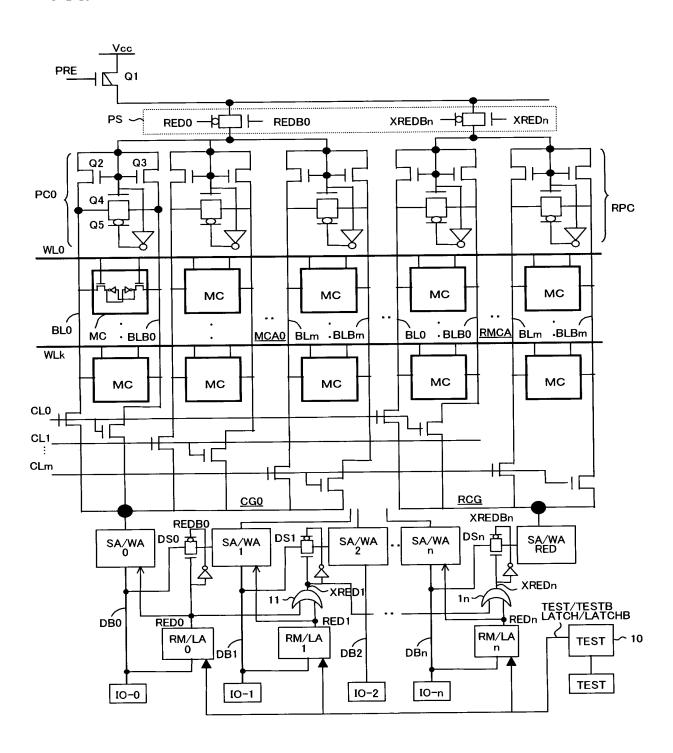
FIRST EMBODIMENT



Title: MEMORY CIRCUIT WITH REDUNDANT MEMORY CELL ARRAY ALLOWING SIMPLIFIED SHIPMENT TESTS AND REDUCED

POWER CONSUMPTIONS Inventor: TANISHIMA et al. Application No. New Docket No. 108066-00096

FIG. 2

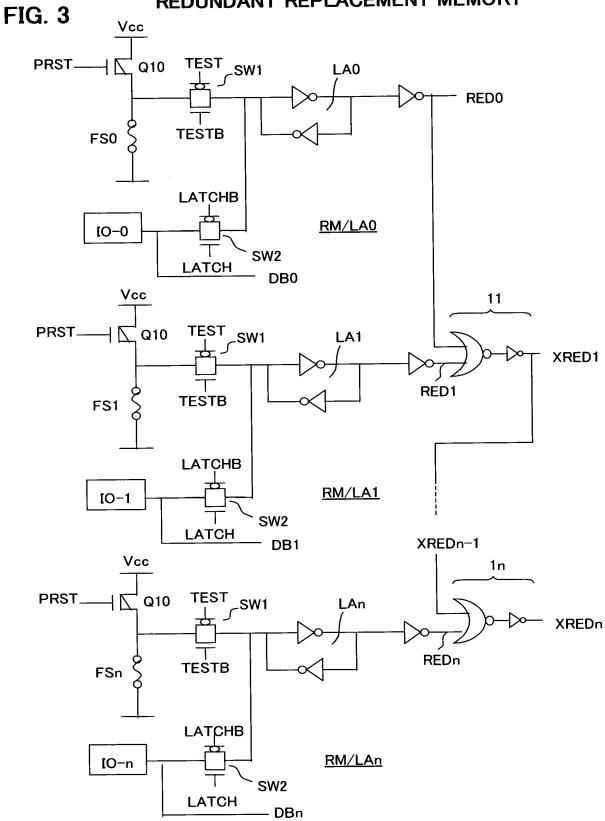


Title: MEMORY CIRCUIT WITH REDUNDANT MEMORY CELL ARRAY ALLOWING SIMPLIFIED SHIPMENT TESTS AND REDUCED

POWER CONSUMPTIONS

Inventor: TANISHIMA et al. Application No. New Docket No. 108066-00096

REDUNDANT REPLACEMENT MEMORY



Title: MEMORY CIRCUIT WITH REDUNDANT MEMORY CELL ARRAY ALLOWING SIMPLIFIED SHIPMENT TESTS AND REDUCED POWER CONSUMPTIONS

Inventor: TANISHIMA et al. Application No. New Docket No. 108066-00096

FIG. 4

TESTING CIRCUIT

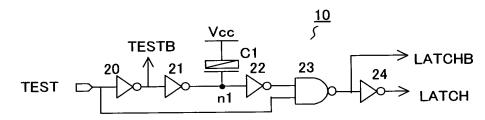
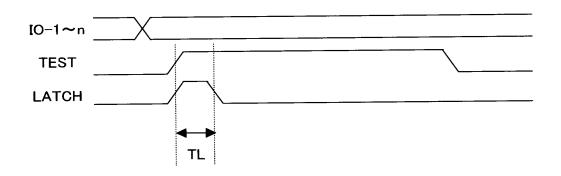


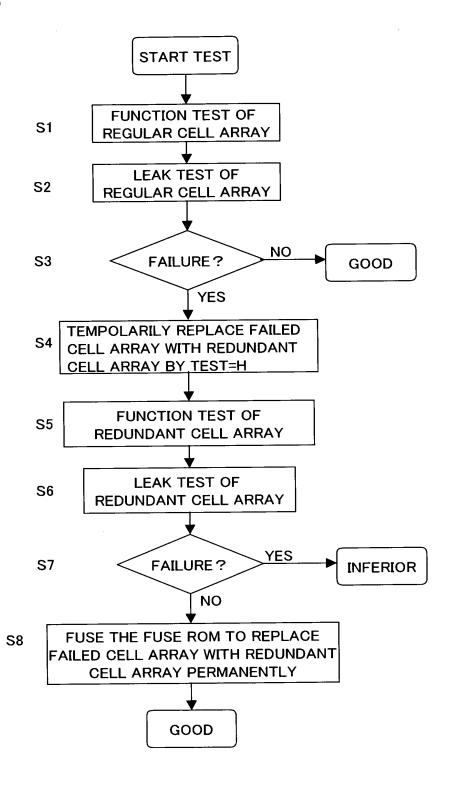
FIG. 5



Title: MEMORY CIRCUIT WITH REDUNDANT MEMORY CELL ARRAY ALLOWING SIMPLIFIED SHIPMENT TESTS AND REDUCED

POWER CONSUMPTIONS Inventor: TANISHIMA et al. Application No. New Docket No. 108066-00096

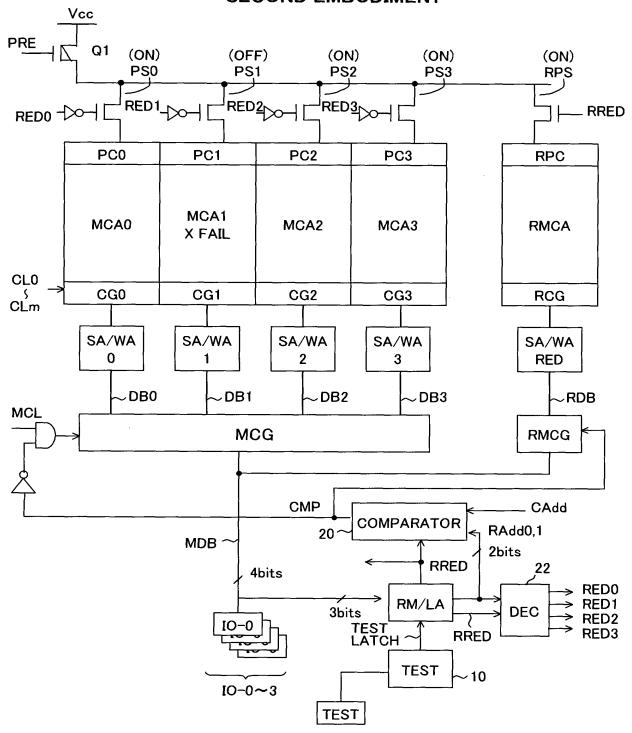
FIG. 6



Title: MEMORY CIRCUIT WITH REDUNDANT MEMORY CELL ARRAY ALLOWING SIMPLIFIED SHIPMENT TESTS AND REDUCED POWER CONSUMPTIONS Inventor: TANISHIMA et al. Application No. New Docket No. 108066-00096

FIG. 7

SECOND EMBODIMENT



Title: MEMORY CIRCUIT WITH REDUNDANT MEMORY CELL ARRAY ALLOWING SIMPLIFIED SHIPMENT TESTS AND REDUCED POWER CONSUMPTIONS Inventor: TANISHIMA et al.

Application No. New Docket No. 108066-00096

